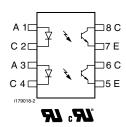


www.vishay.com

Vishay Semiconductors

# Optocoupler, Phototransistor Output, Dual Channel, SOIC-8 Package





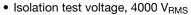
#### **DESCRIPTION**

The ILD205T, ILD206T, ILD207T, ILD211T, and ILD213T are optically coupled pairs with a gallium arsenide infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output. The ILD205T, ILD206T, ILD207T, ILD211T, and ILD213T come in a standard SOIC-8 small outline package for surface mounting which makes it ideally suited for high density applications with limited space. In addition to eliminating through-holes requirements, this package conforms to standards for surface mounted devices.

A specified minimum and maximum CTR allows a narrow tolerance in the electrical design of the adjacent circuits. The high  $BV_{CEO}$  of 70 V gives a higher safety margin compared to the industry standard of 30 V.

#### **FEATURES**

- Two channel coupler
- SOIC-8 surface mountable package
- Standard lead spacing of 0.05"
- Available only on tape and reel option (conforms to EIA standard 481-2)

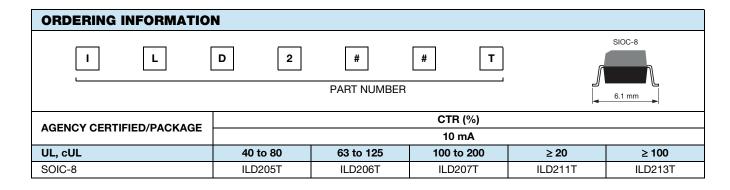




- Compatible with dual wave, vapor phase and IR reflow soldering
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### AGENCY APPROVALS

- UL1577, file no. E52744 system code Y
- cUL file no. E52744, equivalent to CSA bulletin 5A



Vishay Semiconductors

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Peak reverse voltage		$V_R$	6	V			
Peak pulsed current	1 µs, 300 pps		1	Α			
Continuous forward current per channel		I <sub>F</sub>	30	mA			
Power dissipation		P <sub>diss</sub>	50	mW			
OUTPUT							
Collector emitter breakdown voltage		BV <sub>CEO</sub>	70	V			
Emitter collector breakdown voltage		BV <sub>ECO</sub>	7	V			
Power dissipation per channel		P <sub>diss</sub>	125	mW			
COUPLER							
Total package dissipation ambient (2 LEDs and 2 detectors, 2 channels)		P <sub>tot</sub>	350	mW			
Storage temperature		T <sub>stg</sub>	-55 to +150	°C			
Operating temperature		T <sub>amb</sub>	-55 to +100	°C			
Soldering time from 260 °C <sup>(1)</sup>		T <sub>sld</sub>	10	S			

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices.

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	$I_F = 10 \text{ mA}$	$V_{F}$	-	1.2	1.55	V	
Reverse current	V <sub>R</sub> = 6 V	I <sub>R</sub>	-	0.1	100	μΑ	
Capacitance	$V_R = 0 V$	Co	-	25	-	pF	
OUTPUT							
Collector emitter breakdown voltage	$I_C = 10 \mu A$	BV <sub>CEO</sub>	70	-	-	V	
Emitter collector breakdown voltage	$I_E = 10 \mu A$	BV <sub>ECO</sub>	7	-	-	V	
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}$	I <sub>CEO</sub>	-	5	50	nA	
Collector emitter capacitance	V <sub>CE</sub> = 0 V	C <sub>CE</sub>	=	10	-	pF	
COUPLER							
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 2.5 \text{ mA}$	V <sub>CEsat</sub>	=	-	0.4	V	
Capacitance (input to output)		C <sub>IO</sub>	=	0.5	-	pF	

#### Note

Minimum and maximum values were tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	$V_{CE} = 5 \text{ V}, I_F = 10 \text{ mA}$	ILD205T	CTR <sub>DC</sub>	40	-	80	%
		ILD206T	CTR <sub>DC</sub>	63	-	125	%
		ILD207T	CTR <sub>DC</sub>	100	-	200	%
		ILD211T	CTR <sub>DC</sub>	20	-	-	%
		ILD213T	CTR <sub>DC</sub>	100	-	-	%
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 1 mA	ILD205T	CTR <sub>DC</sub>	13	30	-	%
		ILD206T	CTR <sub>DC</sub>	22	45	-	%
		ILD207T	CTR <sub>DC</sub>	34	70	-	%

### Vishay Semiconductors

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t <sub>d</sub>	-	3	-	μs	
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t <sub>r</sub>	-	3	-	μs	
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t <sub>f</sub>	-	4.7	-	μs	
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t <sub>s</sub>	-	0.3	-	μs	
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t <sub>on</sub>	-	6	-	μs	
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t <sub>off</sub>	-	5	-	μs	
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 2)	t <sub>on</sub>	-	3	-	μs	
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 2)	t <sub>off</sub>	-	10	-	μs	

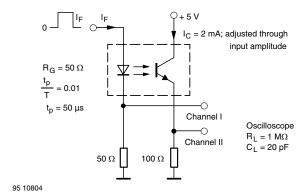


Fig. 1 - Test Circuit, Non-Saturated Operation

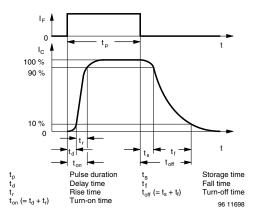


Fig. 3 - Switching Times

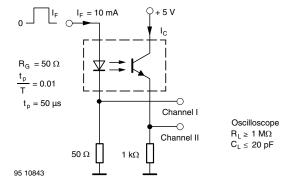


Fig. 2 - Test Circuit, Saturated Operation

### www.vishay.com

### Vishay Semiconductors

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55/100/21				
Comparative tracking index		CTI	175				
Tested withstanding isolation voltage	According to UL1577, t = 1 s	V <sub>ISO</sub>	4000	V <sub>RMS</sub>			
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	6000	V <sub>peak</sub>			
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V <sub>IORM</sub>	560	V <sub>peak</sub>			
Isolation resistance	$T_{amb} = 25  ^{\circ}\text{C},  V_{IO} = 500  \text{V}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω			
Isolation resistance	$T_{amb} = 100  ^{\circ}C,  V_{IO} = 500  V$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω			
Output safety power		P <sub>SO</sub>	350	mW			
Input safety current		I <sub>SI</sub>	150	mA			
Input safety temperature		Ts	165	°C			
Creepage distance	DIP-6, option 6		4	mm			
Clearance distance	DIP-6, option 6		4	mm			
Insulation thickness			0.2	mm			

#### Note

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

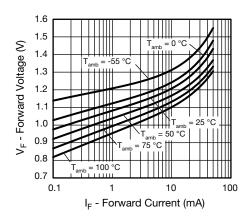


Fig. 4 - Forward Voltage vs. Forward Current

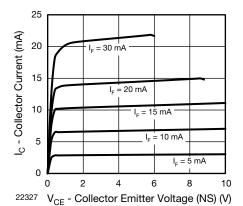


Fig. 5 - Collector Current vs. Collector Emitter Voltage (non-saturated)

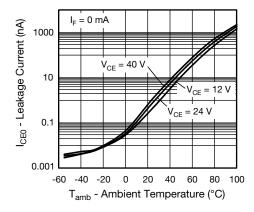


Fig. 6 - Leakage Current vs. Ambient Temperature

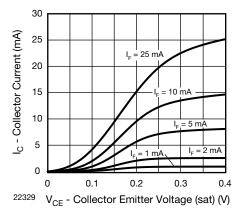


Fig. 7 - Collector Current vs. Collector Emitter Voltage (saturated)

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

### Vishay Semiconductors

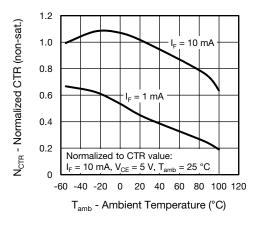


Fig. 8 - Normalized CTR (saturated) vs. Ambient Temperature

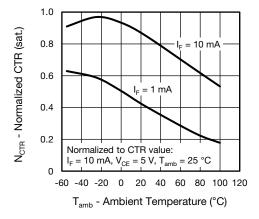


Fig. 9 - Normalized CTR (non-saturated) vs. Ambient Temperature

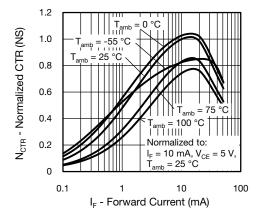


Fig. 10 - Normalized CTR (non-saturated) vs. Forward Current

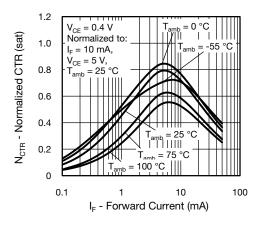


Fig. 11 - Normalized CTR (saturated) vs. Forward Current

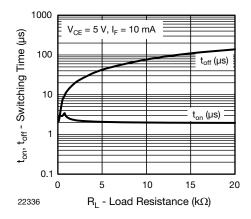


Fig. 12 - Switching Time vs. Load Resistance

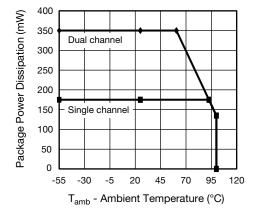
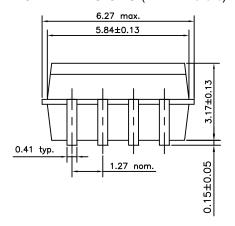


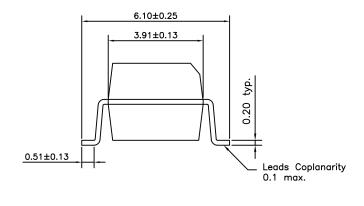
Fig. 13 - Power Dissipation vs. Ambient Temperature

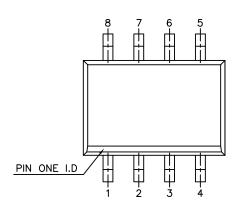


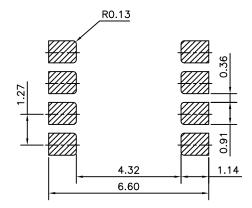
### Vishay Semiconductors

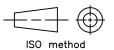
### **PACKAGE DIMENSIONS** (in millimeters)











#### **PACKAGE MARKING** (example)



### Note

• Tape and reel suffix (T) is not part of the package marking.

### Vishay Semiconductors

### TAPE AND REEL PACKAGING FOR DUAL CHANNELS (1) (in millimeters)

Selected dual SOIC8 optocouplers are available in tape and reel format. To order surface mount ILD2XX optocoupler on tape and reel, add a suffix "T" after the part number, i.e., ILD207T.

The tape is 16 mm and is wound on a 33 cm reel. There are 2000 parts per reel. Taped and reeled dual SOIC8 optocouplers conform to EIA-481-2 and IEC 60286-3.

#### Note

(1) Select dual channel devices are available in the shorter SOIC-8 package and will be taped according to the single channel taping specification

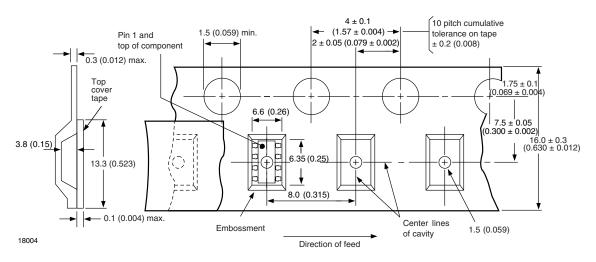


Fig. 14

#### **SOLDER PROFILE**

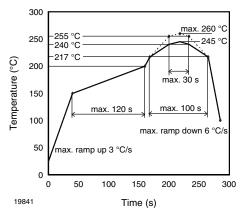


Fig. 15 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020

#### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions: T<sub>amb</sub> < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



### **Legal Disclaimer Notice**

Vishay

### **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.