

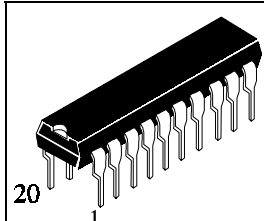
IN74AC374

Octal 3-State  
Noninverting D Flip-Flop  
High-Speed Silicon-Gate CMOS

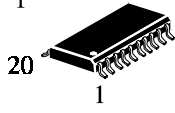
The IN74AC374 is identical in pinout to the LS/ALS374, HC/HCT374. The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LS/ALS outputs.

Data meeting the setup and hold time is clocked to the outputs with the rising edge of the Clock. The Output Enable input does not affect the states of the flip-flops, but when Output Enable is high, the outputs are forced to the high-impedance state; thus, data may be stored even when the outputs are not enabled.

- Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 2.0 to 6.0 V
- Low Input Current: 1.0  $\mu$ A; 0.1  $\mu$ A @ 25°C
- High Noise Immunity Characteristic of CMOS Devices
- Outputs Source/Sink 24 mA



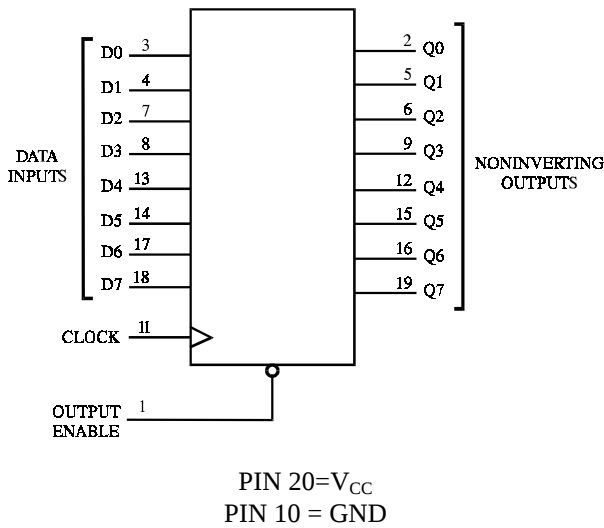
N SUFFIX  
PLASTIC



DW SUFFIX  
SOIC

**ORDERING INFORMATION**  
IN74AC374N Plastic  
IN74AC374DW SOIC  
 $T_A = -40^{\circ}$  to  $85^{\circ}$  C for all packages

LOGIC DIAGRAM



PIN ASSIGNMENT

|                  |    |    |          |
|------------------|----|----|----------|
| OUTPUT<br>ENABLE | 1  | 20 | $V_{CC}$ |
| Q0               | 2  | 19 | Q7       |
| D0               | 3  | 18 | D7       |
| D1               | 4  | 17 | D6       |
| Q1               | 5  | 16 | Q6       |
| Q2               | 6  | 15 | Q5       |
| D2               | 7  | 14 | D5       |
| D3               | 8  | 13 | D4       |
| Q3               | 9  | 12 | Q4       |
| GND              | 10 | 11 | CLOCK    |

FUNCTION TABLE

| Inputs        |                                                                                            |   | Output    |
|---------------|--------------------------------------------------------------------------------------------|---|-----------|
| Output Enable | Clock                                                                                      | D | Q         |
| L             |       | H | H         |
| L             |       | L | L         |
| L             | L,H,  | X | no change |
| H             | X                                                                                          | X | Z         |

X = don't care  
Z = high impedance

**MAXIMUM RATINGS\***

| Symbol    | Parameter                                                                        | Value                  | Unit |
|-----------|----------------------------------------------------------------------------------|------------------------|------|
| $V_{CC}$  | DC Supply Voltage (Referenced to GND)                                            | -0.5 to +7.0           | V    |
| $V_{IN}$  | DC Input Voltage (Referenced to GND)                                             | -0.5 to $V_{CC} + 0.5$ | V    |
| $V_{OUT}$ | DC Output Voltage (Referenced to GND)                                            | -0.5 to $V_{CC} + 0.5$ | V    |
| $I_{IN}$  | DC Input Current, per Pin                                                        | $\pm 20$               | mA   |
| $I_{OUT}$ | DC Output Sink/Source Current, per Pin                                           | $\pm 50$               | mA   |
| $I_{CC}$  | DC Supply Current, $V_{CC}$ and GND Pins                                         | $\pm 50$               | mA   |
| $P_D$     | Power Dissipation in Still Air, Plastic DIP+<br>SOIC Package+                    | 750<br>500             | mW   |
| Tstg      | Storage Temperature                                                              | -65 to +150            | °C   |
| $T_L$     | Lead Temperature, 1 mm from Case for 10 Seconds<br>(Plastic DIP or SOIC Package) | 260                    | °C   |

\*Maximum Ratings are those values beyond which damage to the device may occur.  
Functional operation should be restricted to the Recommended Operating Conditions.

+Derating - Plastic DIP: - 10 mW/°C from 65° to 125°C  
SOIC Package: - 7 mW/°C from 65° to 125°C

**RECOMMENDED OPERATING CONDITIONS**

| Symbol                             | Parameter                                             | Min                                                                        | Max             | Unit            |      |
|------------------------------------|-------------------------------------------------------|----------------------------------------------------------------------------|-----------------|-----------------|------|
| V <sub>CC</sub>                    | DC Supply Voltage (Referenced to GND)                 | 2.0                                                                        | 6.0             | V               |      |
| V <sub>IN</sub> , V <sub>OUT</sub> | DC Input Voltage, Output Voltage (Referenced to GND)  | 0                                                                          | V <sub>CC</sub> | V               |      |
| T <sub>J</sub>                     | Junction Temperature (PDIP)                           |                                                                            | 140             | °C              |      |
| T <sub>A</sub>                     | Operating Temperature, All Package Types              | -40                                                                        | +85             | °C              |      |
| I <sub>OH</sub>                    | Output Current - High                                 |                                                                            | -24             | mA              |      |
| I <sub>OL</sub>                    | Output Current - Low                                  |                                                                            | 24              | mA              |      |
| t <sub>r</sub> , t <sub>f</sub>    | Input Rise and Fall Time *<br>(except Schmitt Inputs) | V <sub>CC</sub> =3.0 V<br>V <sub>CC</sub> =4.5 V<br>V <sub>CC</sub> =5.5 V | 0<br>0<br>0     | 150<br>40<br>25 | ns/V |

\*  $V_{IN}$  from 30% to 70%  $V_{CC}$

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation,  $V_{IN}$  and  $V_{OUT}$  should be constrained to the range  $GND \leq (V_{IN} \text{ or } V_{OUT}) \leq V_{CC}$ .

Unused inputs must always be tied to an appropriate logic voltage level (e.g., either GND or  $V_{CC}$ ). Unused outputs must be left open.

**DC ELECTRICAL CHARACTERISTICS**(Voltages Referenced to GND)

| Symbol           | Parameter                                      | Test Conditions                                                                                                                                 | V <sub>CC</sub><br>V | Guaranteed Limits   |                     | Unit |
|------------------|------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------|----------------------|---------------------|---------------------|------|
|                  |                                                |                                                                                                                                                 |                      | 25 °C               | -40°C to 85°C       |      |
| V <sub>IH</sub>  | Minimum High-Level Input Voltage               | V <sub>OUT</sub> =0.1 V or V <sub>CC</sub> -0.1 V                                                                                               | 3.0<br>4.5<br>5.5    | 2.1<br>3.15<br>3.85 | 2.1<br>3.15<br>3.85 | V    |
| V <sub>IL</sub>  | Maximum Low - Level Input Voltage              | V <sub>OUT</sub> =0.1 V or V <sub>CC</sub> -0.1 V                                                                                               | 3.0<br>4.5<br>5.5    | 0.9<br>1.35<br>1.65 | 0.9<br>1.35<br>1.65 | V    |
| V <sub>OH</sub>  | Minimum High-Level Output Voltage              | I <sub>OUT</sub> ≤ -50 µA                                                                                                                       | 3.0<br>4.5<br>5.5    | 2.9<br>4.4<br>5.4   | 2.9<br>4.4<br>5.4   | V    |
|                  |                                                | *V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OH</sub> =-12 mA                                                                 | 3.0                  | 2.56                | 2.46                |      |
|                  |                                                | I <sub>OH</sub> =-24 mA                                                                                                                         | 4.5                  | 3.86                | 3.76                |      |
|                  |                                                | I <sub>OH</sub> =-24 mA                                                                                                                         | 5.5                  | 4.86                | 4.76                |      |
| V <sub>OL</sub>  | Maximum Low-Level Output Voltage               | I <sub>OUT</sub> ≤ 50 µA                                                                                                                        | 3.0<br>4.5<br>5.5    | 0.1<br>0.1<br>0.1   | 0.1<br>0.1<br>0.1   | V    |
|                  |                                                | *V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OL</sub> =12 mA                                                                  | 3.0                  | 0.36                | 0.44                |      |
|                  |                                                | I <sub>OL</sub> =24 mA                                                                                                                          | 4.5                  | 0.36                | 0.44                |      |
|                  |                                                | I <sub>OL</sub> =24 mA                                                                                                                          | 5.5                  | 0.36                | 0.44                |      |
| I <sub>IN</sub>  | Maximum Input Leakage Current                  | V <sub>IN</sub> =V <sub>CC</sub> or GND                                                                                                         | 5.5                  | ±0.1                | ±1.0                | µA   |
| I <sub>OZ</sub>  | Maximum Three-State Leakage Current            | V <sub>IN</sub> (OE)= V <sub>IH</sub> or V <sub>IL</sub><br>V <sub>IN</sub> =V <sub>CC</sub> or GND<br>V <sub>OUT</sub> =V <sub>CC</sub> or GND | 5.5                  | ±0.5                | ±5.0                | µA   |
| I <sub>OLD</sub> | +Minimum Dynamic Output Current                | V <sub>OLD</sub> =1.65 V Max                                                                                                                    | 5.5                  |                     | 75                  | mA   |
| I <sub>OHD</sub> | +Minimum Dynamic Output Current                | V <sub>OHD</sub> =3.85 V Min                                                                                                                    | 5.5                  |                     | -75                 | mA   |
| I <sub>CC</sub>  | Maximum Quiescent Supply Current (per Package) | V <sub>IN</sub> =V <sub>CC</sub> or GND                                                                                                         | 5.5                  | 8.0                 | 80                  | µA   |

\* All outputs loaded; thresholds on input associated with output under test.

+Maximum test duration 2.0 ms, one output loaded at a time.

Note: I<sub>IN</sub> and I<sub>CC</sub> @ 3.0 V are guaranteed to be less than or equal to the respective limit @ 5.5 V V<sub>CC</sub>

**AC ELECTRICAL CHARACTERISTICS**( $C_L=50\text{pF}$ , Input  $t_r=t_f=3.0\text{ ns}$ )

| Symbol           | Parameter                                        | V <sub>CC</sub> <sup>*</sup><br>V | Guaranteed Limits |              |               |              | Unit |
|------------------|--------------------------------------------------|-----------------------------------|-------------------|--------------|---------------|--------------|------|
|                  |                                                  |                                   | 25 °C             |              | -40°C to 85°C |              |      |
|                  |                                                  |                                   | Min               | Max          | Min           | Max          |      |
| f <sub>max</sub> | Maximum Clock Frequency (Figure 1)               | 3.3<br>5.0                        | 60<br>100         |              | 60<br>100     |              | MHz  |
| t <sub>PLH</sub> | Propagation Delay, Clock to Q (Figure 1)         | 3.3<br>5.0                        | 3.0<br>2.5        | 13.5<br>9.5  | 1.5<br>1.5    | 15.5<br>10.5 | ns   |
| t <sub>PHL</sub> | Propagation Delay, Clock to Q (Figure 1)         | 3.3<br>5.0                        | 2.5<br>2.0        | 12.5<br>9.0  | 2.0<br>1.5    | 14.0<br>10.0 | ns   |
| t <sub>PZH</sub> | Propagation Delay, Output Enable to Q (Figure 2) | 3.3<br>5.0                        | 3.0<br>2.0        | 11.5<br>8.5  | 1.5<br>1.0    | 13.0<br>9.5  | ns   |
| t <sub>PZL</sub> | Propagation Delay, Output Enable to Q (Figure 2) | 3.3<br>5.0                        | 2.5<br>2.0        | 11.5<br>8.5  | 1.5<br>1.0    | 13.0<br>9.5  | ns   |
| t <sub>PHZ</sub> | Propagation Delay, Output Enable to Q (Figure 2) | 3.3<br>5.0                        | 3.0<br>2.0        | 12.5<br>11.0 | 2.0<br>2.0    | 14.5<br>12.5 | ns   |
| t <sub>PLZ</sub> | Propagation Delay, Output Enable to Q (Figure 2) | 3.3<br>5.0                        | 2.0<br>1.5        | 11.5<br>8.5  | 1.0<br>1.0    | 12.5<br>10.0 | ns   |
| C <sub>IN</sub>  | Maximum Input Capacitance                        | 5.0                               | 4.5               |              | 4.5           |              | pF   |

| C <sub>PD</sub> | Power Dissipation Capacitance | Typical @25°C, V <sub>CC</sub> =5.0 V | pF |
|-----------------|-------------------------------|---------------------------------------|----|
|                 |                               | 80                                    |    |

\*Voltage Range 3.3 V is 3.3 V  $\pm 0.3\text{ V}$ Voltage Range 5.0 V is 5.0 V  $\pm 0.5\text{ V}$ **TIMING REQUIREMENTS**( $C_L=50\text{pF}$ , Input  $t_r=t_f=3.0\text{ ns}$ )

| Symbol          | Parameter                                   | V <sub>CC</sub> * | Guaranteed Limits |               | Unit |
|-----------------|---------------------------------------------|-------------------|-------------------|---------------|------|
|                 |                                             | V                 | 25 °C             | -40°C to 85°C |      |
|                 |                                             |                   |                   |               |      |
| t <sub>su</sub> | Minimum Setup Time,Data to Clock (Figure 3) | 3.3               | 5.5               | 6.0           | ns   |
|                 |                                             | 5.0               | 4.0               | 4.5           |      |
| t <sub>h</sub>  | Minimum Hold Time, Clock to Data (Figure 3) | 3.3               | 1.0               | 1.0           | ns   |
|                 |                                             | 5.0               | 1.5               | 1.5           |      |
| t <sub>w</sub>  | Minimum Pulse Width, Clock (Figure 1)       | 3.3               | 5.5               | 6.0           | ns   |
|                 |                                             | 5.0               | 4.0               | 4.5           |      |

\*Voltage Range 3.3 V is 3.3 V  $\pm 0.3\text{ V}$ Voltage Range 5.0 V is 5.0 V  $\pm 0.5\text{ V}$

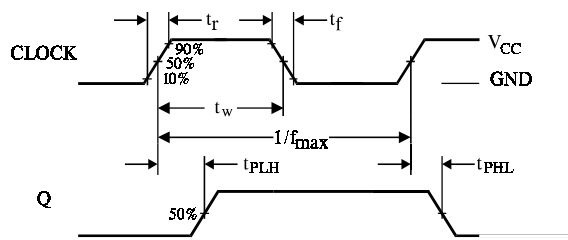


Figure 1. Switching Waveforms

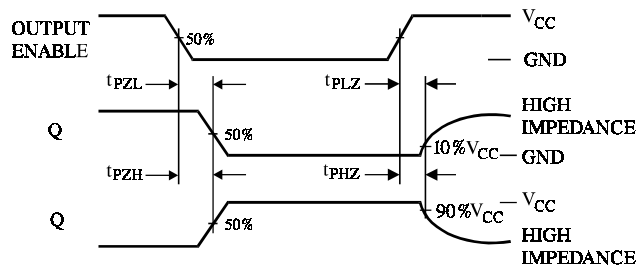


Figure 2. Switching Waveforms

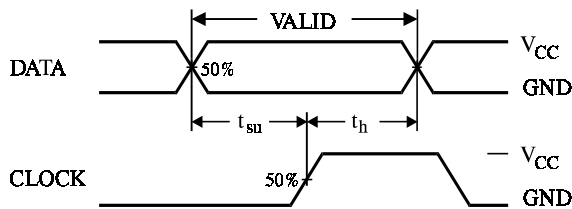


Figure 3. Switching Waveforms

EXPANDED LOGIC DIAGRAM

